

# Logic Pod

## Enhanced Functionality POD

Logic input signals D0, D1, D2 now are also wired to DX0...DX2 directly from PIC 16F877. This allows these pins to be used as:-

1. Direct PIC inputs bypassing Sample RAM
2. 4 multiplexed 10 bit A/D converter inputs (0-5V)
3. 4 digital outputs from PIC for signal generation.

10MS/s DAC can be enabled out on D4

D7 can be driven with TRIG signal to slave other BitScope devices

D6 can be driven with ZZ-CLK for external capture options.

