Matching JFET devices

Pairs of JFETs (Q6/Q7, Q8/Q9) should be reasonably matched to minimise temperature drift of input stage.

To match these devices, measure the current with Vgs = -0.6V (1 diode Vf).

If possible, these pairs should match within 5%.

Calibrate to 0V without input

16Hz Prescaler

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